

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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n resepplication of: Alan J. Janicek, et al.

Serial Number: 09/682,765

Filed: October 16, 2001

For: QUICK-CONNECT POSITIVE

TEMPERATURE COEFFICIENT OF RESISTANCE RESISTOR/OVERLOAD

ASSEMBLY AND METHOD

Group Art Unit: 2832

Examiner: Karl Easthom

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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I hereby certify that the original paper identified herein is being deposited with United States Postal Service as first class mail, postage prepaid, to Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450

on: \_\_\_\_

Bradlev D. Filis

## **AMENDMENT**

Dear Sir:

In response to the Official Action dated November 5, 2005 ("Official Action"), Applicants respond as follows:

03/14/2005 EAREGAY1 00000025 500605 09682765

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